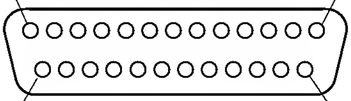
HITACHI

FLORA

FLORA

[illegible]



[illegible]	[illegible]	[illegible]	[illegible]
1	STROBE-N	DATA0～DATA7 [illegible]	[illegible]
2	DATA0	[illegible]	[illegible]
3	DATA1		
4	DATA2		
5	DATA3		
6	DATA4		
7	DATA5		
8	DATA6		
9	DATA7		
10	ACKNLG-N	[illegible]	[illegible]
11	BUSY	[illegible]	[illegible]
12	PE	[illegible]	[illegible]
13	SLCT	[illegible]	[illegible]
14	AUTO FD-N	[illegible]	[illegible]
15	ERROR-N	[illegible]	[illegible]
16	INIT-N	[illegible]	[illegible]
17	SLCTIN-N	[illegible] DC1/DC3 [illegible]	[illegible]
18～25	GND	[illegible]	—

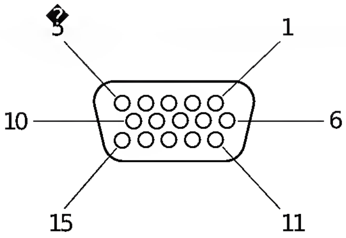
[illegible]: [illegible] BIOS [illegible]

ヒント

[illegible] -N [illegible]

[illegible]

[illegible]



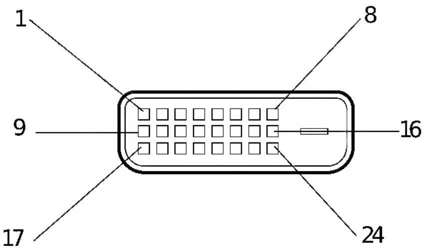
[illegible]	[illegible]	[illegible]	[illegible]
1	R	[illegible]	[illegible]
2	G	[illegible]	[illegible]
3	B	[illegible]	[illegible]
4	NC	[illegible]	–
5	GND	[illegible]	–
6	R-GND	[illegible]	–
7	G-GND	[illegible]	–
8	B-GND	[illegible]	–
9	+5V	[illegible]	[illegible]
10	GND	[illegible]	–
11	NC	[illegible]	–
12	SDA	[illegible]	[illegible]
13	HSYNC	[illegible]	[illegible]
14	VSYNC	[illegible]	[illegible]
15	SCL	[illegible]	[illegible]

ヒント

[illegible]

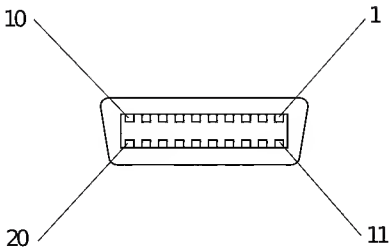
[illegible]

DVI [illegible]



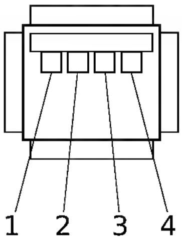
[illegible]	[illegible]	[illegible]
1	T.M.D.S. Data2-	T.M.D.S. [illegible] 2-
2	T.M.D.S. Data2+	T.M.D.S. [illegible] 2+
3	T.M.D.S. Data2 SHIELD	T.M.D.S. [illegible] 2[illegible]
4	Reserve	[illegible]
5	Reserve	[illegible]
6	DDC Clock	DDC[illegible]
7	DDC Data	DDC[illegible]
8	No Connect	[illegible]
9	T.M.D.S. Data1-	T.M.D.S. [illegible] 1-
10	T.M.D.S. Data1+	T.M.D.S. [illegible] 1+
11	T.M.D.S. Data1 SHIELD	T.M.D.S. [illegible] 1[illegible]
12	Reserve	[illegible]
13	Reserve	[illegible]
14	+5V Power	+5V[illegible]
15	Ground(for +5V)	[illegible] (+5V[illegible]
16	Hot Plug Detect	[illegible]
17	T.M.D.S. Data0-	T.M.D.S. [illegible] 0-
18	T.M.D.S. Data0+	T.M.D.S. [illegible] 0+
19	T.M.D.S. Data0 SHIELD	T.M.D.S. [illegible] 0[illegible]
20	Reserve	[illegible]
21	Reserve	[illegible]
22	T.M.D.S. Clock SHIELD	T.M.D.S. [illegible]
23	T.M.D.S. Clock+	T.M.D.S. [illegible] +
24	T.M.D.S. Clock-	T.M.D.S. [illegible] -

DFP □ □ □ □ □



□ □	□ □ □	□ □	□ □
1	TX1 +	TMDS□ □ □ (G) □ □ □ □ □ □ □	□ □
2	TX1 -	TMDS□ □ □ (G) □ □ □ □ □ – □	□ □
3	TX1 SHIELD	TMDS□ □ □ □	–
4	TXC SHIELD	TMDS□ □ □ □	–
5	TXC +	TMDS□ □ □ □ □ □ □ □ □ □ □	□ □
6	TXC -	TMDS□ □ □ □ □ □ □ □ □ – □	□ □
7	GND	DDC□ □ □ GND	–
8	+5VDC	DDC□ □ □ +5V	□ □
9	Reserved	□ □	–
10	Reserved	□ □	–
11	TX2 +	TMDS□ □ □ (R) □ □ □ □ □ □ □	□ □
12	TX2 -	TMDS□ □ □ (R) □ □ □ □ □ – □	□ □
13	TX2 SHIELD	TMDS□ □ □ □	–
14	TX0 SHIELD	TMDS□ □ □ □	–
15	TX0 +	TMDS□ □ □ (B) □ □ □ □ □ □ □	□ □
16	TX0 -	TMDS□ □ □ (B) □ □ □ □ □ – □	□ □
17	Reserved	□ □	–
18	SENSE	□ □ □ □ □ □ □ □ □	□ □
19	DDC / SDA	DDC□ □ □	□ □ □
20	DDC / SCL	DDC□ □ □ □	□ □

USB□□□□□

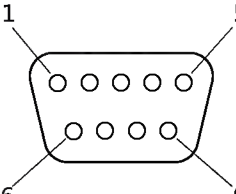


□□	□□□	□□	□□
1	+5V	□□□□□□	□□
2	DATA-	□□□□－	□□□
3	DATA+	□□□□□	□□□
4	GND	□□□□□□□□□	－

ヒント

□ USB□□Universal Serial Bus□□□□□□

□□□□□□□□□□□□□□□□

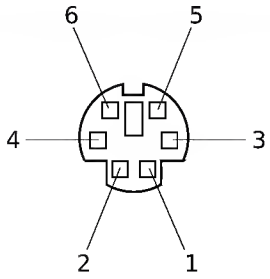


□□	□□□	□□	□□
1	DCD	□□□□□□	□□
2	RD	□□□□□	□□
3	TD	□□□□□	□□
4	DTR	□□□□□□□□	□□
5	GND	□□□□□□	－
6	DSR	□□□□□□□□□	□□
7	RTS	□□□□	□□
8	CTS	□□□	□□
9	RI	□□□□	□□

ヒント

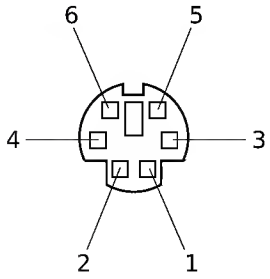
□ □□□□□□□□□□□□□□□□□

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □



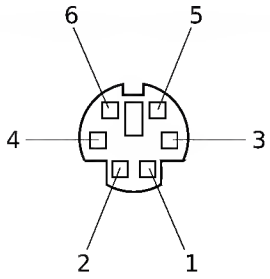
□ □	□ □ □	□ □	□ □
1	DATA(K/B)	□ □ □ □ □ □ □	□ □ □
2	NC	□ □ □ □	–
3	GND	□ □ □ □ □	–
4	+5V *	□ □	□ □
5	Clock(K/B)	□ □ □ □ □ □ □ □	□ □ □
6	NC	□ □ □ □	–

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □



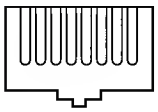
□ □	□ □ □	□ □	□ □
1	DATA(MS)	□ □ □ □ □	□ □ □
2	NC	□ □ □ □	–
3	GND	□ □ □ □ □	–
4	+5V	□ □	□ □
5	Clock(MS)	□ □ □ □ □ □	□ □ □
6	NC	□ □ □ □	–

□ □ □ / □

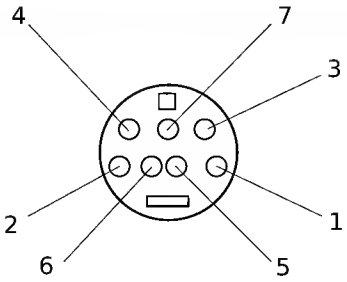


□□	□□□	□□	□□
1	DATA(MS)	□□□□□□	□□□
2	DATA(KB)	□□□□□□□□	□□□
3	GND	□□□□□□	–
4	+5V	□□	□□
5	Clock(MS)	□□□□□□□	□□□
6	Clock(KB)	□□□□□□□□	□□□

LAN□ □ □ □ □ □ □ □ □ □ □ □



□□	□□□	□□	□□
1	TD+	□□□□□□	□□
2	TD-	□□□□□–	□□
3	RD+	□□□□□□	□□
4	GND	□□□□□□ GND	–
5	GND	□□□□□□ GND	–
6	RD-	□□□□□–	□□
7	GND		–
8	GND		–



□□	□□□	□□
1	GND	—
2	GND	—
3	Y□□	□□
4	C□□	□□
5	□□	—
6	□□	—
7	□□□□□□□	□□